PCN Number:			2	0170609	001				P	CN Da	ite:	June 13, 2017
			on of	of TIPI as additional Assembly and Test Site for Select Devices								
Cus	tome	r Contact:		N Manage		Dep		Quality Servi				
Pro	posed	1 st Ship D	ate:				Estimated	Estimated Sample Date Provided at Sample request				
Cha	nge T	уре:										
		mbly Site					Design	☐ Wafer Bump Site				
		mbly Proces					Data Shee					
		mbly Materia						ber change				
		anical Speci					Test Site					ab Site
	Packi	ng/Shipping	J/Labe	eling			Test Proce	ess	ŀ	Wafer Fab Materials		
						D/	CN Detai	le .	L	wa	rer Fa	ab Process
Doc	crinti	on of Chan	ao:			P	IN Detai	15				
				linnines)	as	additi	onal Asser	nbly and Test	Site	o for S	elect	Devices listed
												are as follows.
							,					
	Asse	mbly Site	Asse	embly Sit	e Or	igin	Assembly	sembly Country Code		As	semb	ly Site City
	JCE ⁻	T Co. Ltd		JCE				CHN		Jiangyin		
•	TI Phili	ippines A/T		PHI			PHL		Baguio City			
Mat	eria <u>l</u>	Differences	5:									
				J	CET	1	1	TIPI				
		lold compou	ınd	120800005407			22198					
	L	ead finish		Matte Sn		N	iPdAu					
Test	cover	age, inserti	ons, c	condition	s wi	II rem	nain consis	tent with curre	ent	testin	g and	I verified with
	Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ.											
Rea	son fo	or Change:										
Cont	tinuity	of Supply										
Anti	icipat	ed impact	on Fo	rm, Fit,	Fu	nctio	n, Quality	or Reliabilit	y (positi	ve /	negative):
Non	None											
Anti	Anticipated impact on Material Declaration											
Material Declaration			r c r v	rod elea btai nate vith	uction use. I ined f erial r this f	n data and Jpon produ Trom the Ti neeting cur PCN change	will be availab action release Eco-Info web rrent regulaton e.	ole the site	follow revise . The	ing the ed represented	ports can be no impact to the	
Cha	Changes to product identification resulting from this PCN:											

Assembly Site					
JCET Co. Ltd	Assembly Site Origin (22L)	ASO: JCE			
TI Philippines A/T	Assembly Site Origin (22L)	ASO: PHI			

Sample product shipping label (not actual product label)



ASSEMBLY SITE CODES: JCET = F, TIPI = W

Product Affected:

TPS561201DDCR	TPS561208DDCT	TPS562208DDCR	TPS563201DDCT
TPS561201DDCT	TPS562201DDCR	TPS562208DDCT	TPS563208DDCR
TPS561208DDCR	TPS562201DDCT	TPS563201DDCR	TPS563208DDCT

Qualification Data TIPI SOT: Phase 3 (DDC_FC0L_JCAP Bump)

Approve Date 07-Jun-2017

Product Attributes

Attributes	Qual Device: TPS562201DDCR
Assembly Site	PHI (TIPI)
Package Family	SOT
Flammability Rating	UL 94 V-0
Wafer Fab Supplier	MIHO8
Wafer Fab Process	LBC7.3

⁻ QBS: Qual By Similarity

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TPS562201DDCR
ED	Electrical Characterization	Per Datasheet Parameters	Pass
FLAM	Flammability (UL 94V-0)		3/15/0
HAST	Biased HAST, 130C/85%RH	96 Hours	3/231/0
HTOL	Life Test, 125C	1000 Hours	3/231/0
HTSL	High Temp Storage Bake 170C	420 Hours	3/231/0
LI	Lead Fatigue	Leads	3/66/0
LI	Lead Pull	Leads	3/66/0

⁻ Qual Device TPS562201DDCR is qualified at LEVEL1-260C

MISC	Salt Atmosphere		3/66/0
MQ	Manufacturability (Assembly)	(per mfg. Site specification)	Pass
MQ	Manufacturability (Bump)	(per mfg. Site specification)	Pass
PD	Physical Dimensions		3/15/0
SD	Solderability	8 Hours Steam Age	3/36/0
TC	Temperature Cycle, -65/150C	500 Cycles	3/231/0
UHAST	Unbiased HAST 130C/85%RH	96 Hours	3/231/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

Qualification Data TIPI SOT: Phase3 (DDC_FCOL_JCAP_Bump_TPS563201DDCR)

Approve Date 08-Jun-2017

Product Attributes

Attributes	Qual Device: TPS563201DDCR	
Assembly Site	PHI (TIPI)	
Package Family	SOT	
Flammability Rating	UL 94 V-0	
Wafer Fab Supplier	MIHO 8	
Wafer Fab Process	LBC7.3	

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	TPS563201DDCR
LI	Lead Fatigue	Leads	3/66/0
LI	Lead Pull	Leads	3/66/0
MQ	Manufacturability (Assembly)	(per mfg. Site specification)	Pass
MQ	Manufacturability (Bump)	(per mfg. Site specification)	Pass
PD	Physical Dimensions		3/15/0
TC	Temperature Cycle, -65/150C	500 Cycles	3/231/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com